

Typical internal trigger event from NCC stripixel FEM with 2 SVX4. Taken using home-made 'chipskop' embedded in FPGA. One tick is 6.25ns (1/16 of FEClk).

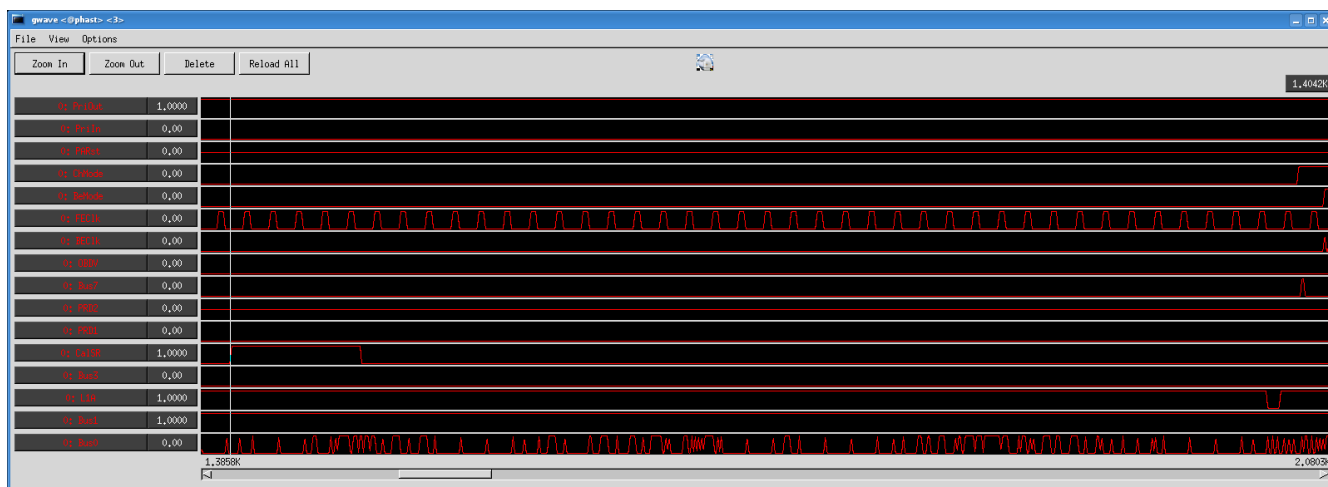


Fig 1: Shown CalSR at tick 1403, L1A at 2040

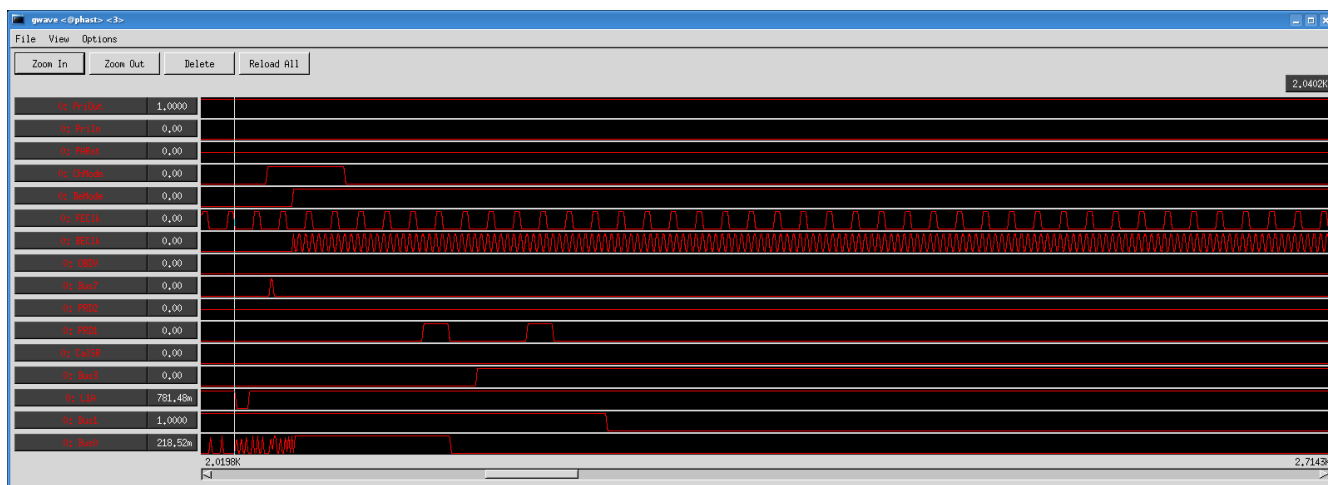


Fig 2: Continuation of Fig.1. Shown are L1A at 2040, ChMode at 2059, BeMode 2075, PRD1 up @ 2156 down @ 2173, Bus0 down @2174, Bus3 up @2188, 2nd PRD1 up @2219 down @2235, Bus1 down @2268,

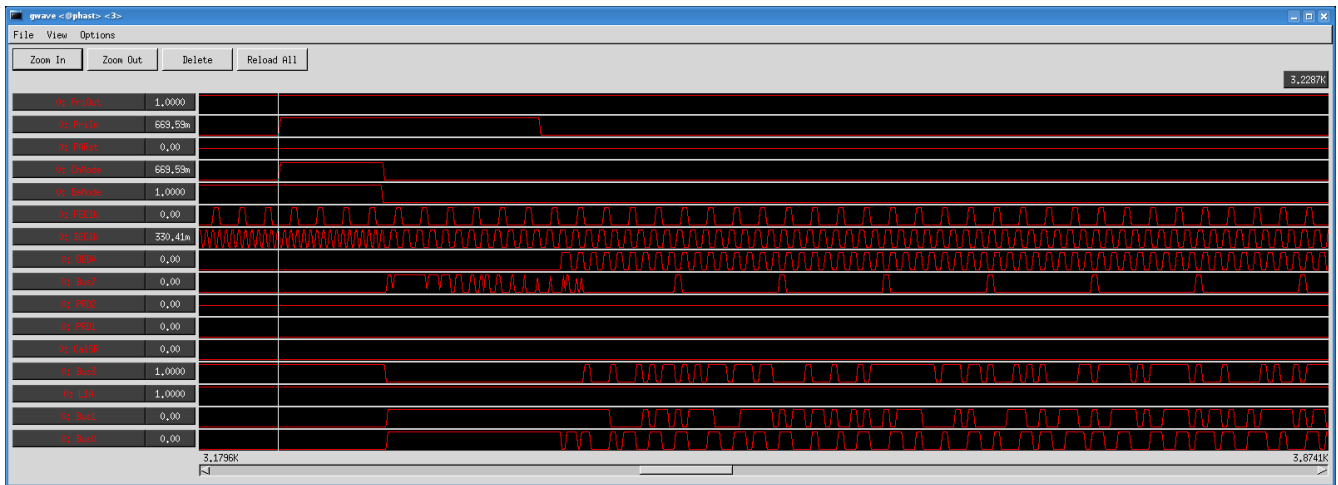


Fig 3: Continuation. PrIn & ChMode @3228, Bemode down and BEClk frequency change @3291, ChMode down @3292, Some garbage due to switching bus, PrIn down @3388, OBDV arrives at 3401 while bus is still noisy, we often losing first couple of words.

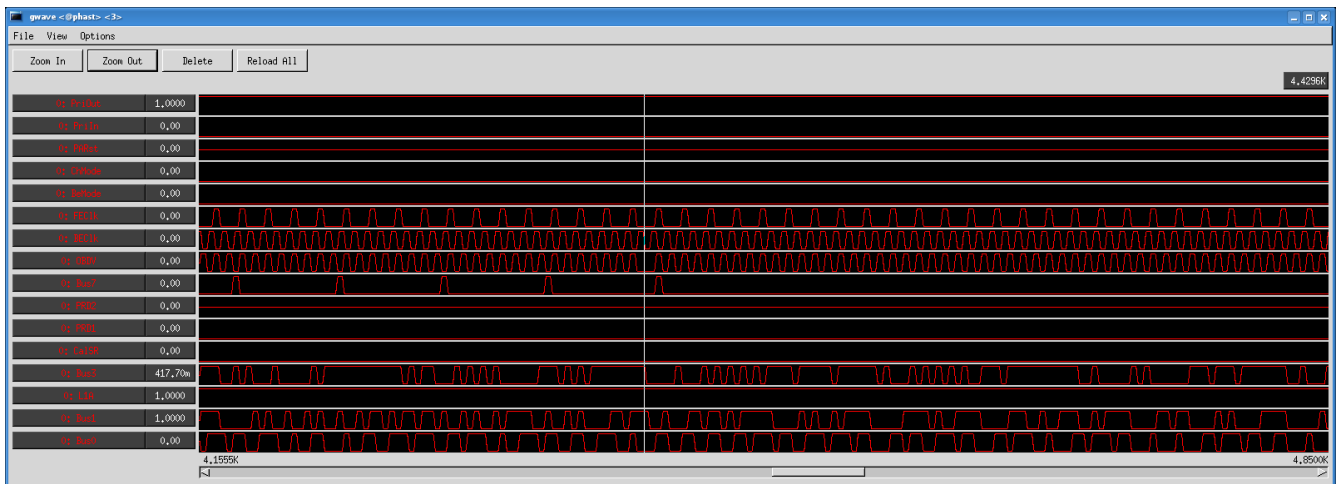


Fig 4: Continuation. Chip are passing the token @4429

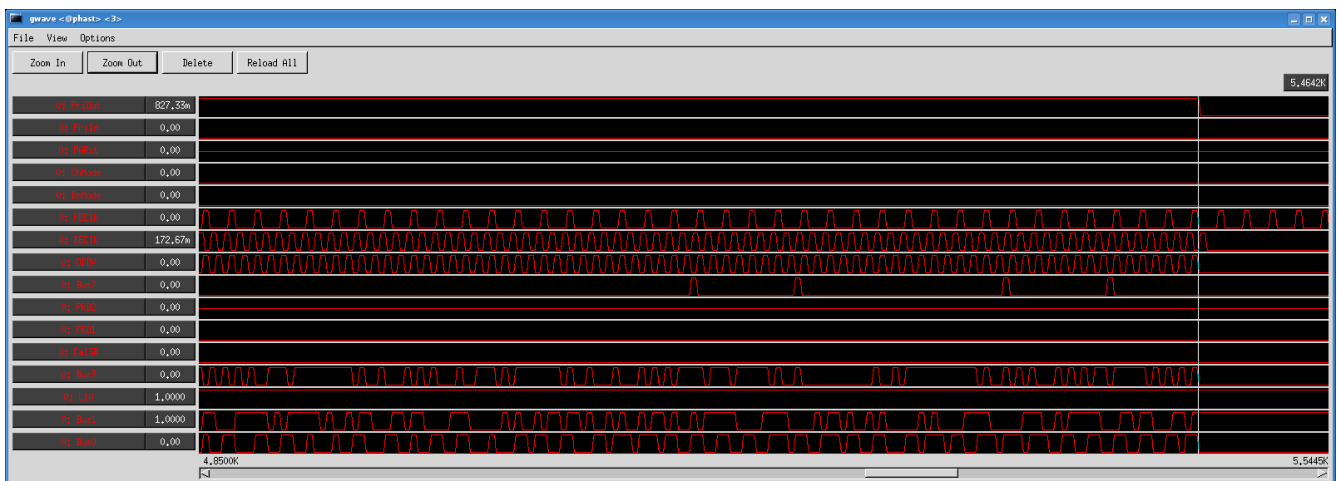


Fig 5: Continuation. PriOut down @5464. End of readout.